

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Anuj DUA, et al.

Confirmation No.: 9068

Application No.: 10/717,671

Examiner: HUISMAN, DAVID J

Filing Date: 11/21/2003

Group Art Unit: 2183

Title: Method and apparatus for fetching instructions from the memory subsystem of a mixed architecture processor into a hardware emulation engine

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

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RESPONSIVE TO A DOUBLE PATENTING REJECTION

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Respectfully submitted,

Anuj DUA, et al.

By _____

Les P. Gehman,

Attorney/Agent for Applicant(s)

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